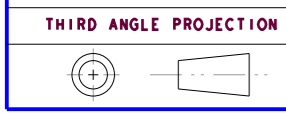


- Notes:
1. Printing always on cold side.
 2. Tolerance of thermo and electric parameters $\pm 10\%$.
 3. Please mount heat sink before you use it. also, please do not exceed the extra voltage at any time.
 4. Please contact with us if you need Melting Point 183°C (Operation Temperature 150°C Max.).

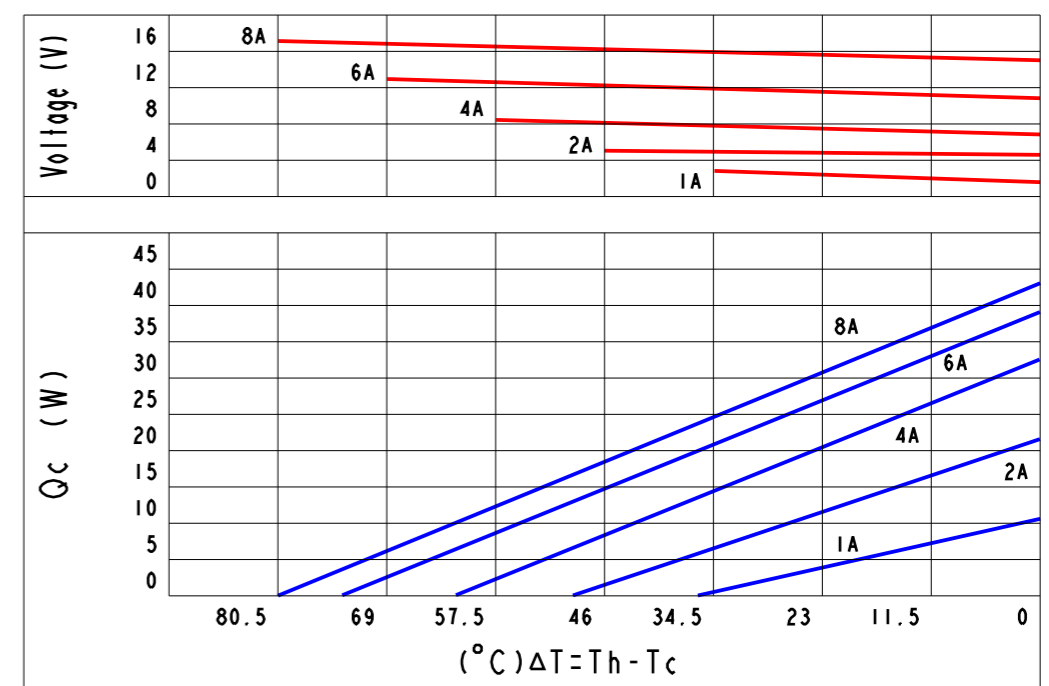
*DO NOT SCALE DRAWING



THIS DRAWING AND THE DATA DISCLOSED HEREIN OR HERewith IS NOT TO BE REPR ODUCEd USED OR DISCLOSED OR IN PART TO ANYONE WITHOUT THE PERMISSION OF KJLP (SHENZHEN) ELECTRONICS CO., LTD.

REVISIONS					
REV.	POS.	DESCRIPTION	DATE	DRW	APP
A		INITIAL CREATION	2013/09/09	Gory	Mason

Curve Chart(Be Confined To TEC2-196084040):




Part Number And Feature:

T	E	C	2	-	1	9	6	x	x	4	0	4	0	Sealing	YES
↓	↓	↓	↓		↓	↓	↓	↓	↓	↓	↓	↓	↓	Operation Temperature	125°C (Max.)
Thermo	Electric	Chip	Stage	Stack	N & P	Stack	Quantity	Current	A(Max.)	Dimension	(A)	Dimension	(B)	Melting Point	138°C
														Storage Temperature	$-60^{\circ}\text{C}\sim 100^{\circ}\text{C}$
														RoHS	YES

Technical Data:

ITEM	Part NO.	Stack(P&N)	A(Max.)	V(Max.)	Qc(W) /Th=27°C/ ΔT(°C)	DIM(A)	DIM(B)	DIM(H)	
1	TEC2-196044040	196	4 A	16 V	23W	80°C	40	40	RF7.6
2	TEC2-196064040	196	6 A	16 V	33W	80°C	40	40	RF6.9
3	TEC2-196084040	196	8 A	16 V	43W	80°C	40	40	RF6.4

1. UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE MM
2 TOLERANCE ARE AS FOLLOWS:
0 < X < 2 ± 0.06
2 < X < 10 ± 0.08
10 < X < 50 ± 0.12
50 < X < 100 ± 0.16
100 < X < 200 ± 0.20
200 < X < 300 ± 0.30
ANGLES ± 0.5°

PART No.	TEC2-196xx4040	DESCRIPTION	DC 16V(Max.),4~8A(Max.),196 P&N,40*40mm			
SIGNATURE	DATE	 昆晶冷片(深圳)电子有限公司 KJLP(SHENZHEN)ELECTRONICS CO., LTD email: kjlp@kjlp.net http:// www.kjlp.net Tel: +86-755-82528352 Fax: +86-755-22639899				
DRAWN BY	Gory					2013/09/09
CHECKED BY	Justin					2013/09/09
ENGR	Vivi					2013/09/09
APPROVED BY	Mason					2013/09/09
ISSUED BY	Jack	2013/09/09	CAD MODLE:	TEC2-196xx4040.prt	SCALE: 1:1	REV: A
MATERIAL:		CAD DWG:	TEC2-196xx4040.drw	SIZE: A3	SHEET: 1 OF 1	